Shashank Jain

Electronics Engineer

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🏠 Panchkula, Haryana, India

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Technical Skills

Beginner ⟶ *Expert*

Analog Layout, QuestaSim, CMOS

C, Python, Verilog, Linux, TCL

PCB Testing, Circuit Design, LabVIEW

C++, System Verilog, UVM, NgSpice

Languages

English : Fluent (C1)

Hindi : Native

Publications

Design of a low-power 3rd order notch filter for biomedical applications

IEEE Electron Device Kolkata Conference

N. Sharma; S. Jain; K. Sharma; S. Malhotra

📅 2022 🔗 10.1109/EDKCON56221.2022.10032908

Certifications

Introduction to Cybersecurity

📅 2022 🔗 CISCO

Networking Essentials

📅 2021 🔗 CISCO

Hobbies

Sports : Basketball, Pocket Billiards

Games : Sudoku, puzzles

Objective

Highly motivated VLSI Design engineer with a solid understanding of CMOS technology, layout design, and Verilog. Eager to join a dynamic team and gain experience in the semi-conductor industry.

Education

Mar 2024 - Aug 2024
Postgraduate Diploma in VLSI
Centre for Development of Advanced Computing (CDAC), ACTS, Pune, India

Aug 2019 - Jun 2023
B.E. in Electronics and Communication Engineering
Chitkara University, Punjab, India
GPA : 9.19/10.0

Work Experience

Jan 2023 - Jun 2023
Testing Intern | *Bharat Electronics Limited (BEL), Panchkula, India*
- Conducted PCB assessments using oscilloscopes/DMMs, identifying defects, and managing component replacements under strict quality control, improving efficiency.
- Troubleshooted and evaluated board functionality, demonstrating strong hands-on skills.
- Performed maintenance and supported system upgrades, adhering to safety protocols to minimize downtime.

Projects

Aug 2024
AXI4 To SDIO Bridge Controller | *CDAC ACTS, Pune, India*
- Developed testbenches using Questa Sim and Vivado to validate an AXI4 to SDIO bridge controller.
- Ensured the controller's functionality and performance met design requirements.

Jul 2022 - Dec 2022
Design of a low-power 3rd order notch filter for biomedical applications (B.E. Thesis) | *Chitkara University, Punjab, India*
- Developed an analog layout design for a low-power notch filter using Cadence Virtuoso.
- Modernized an OTA circuit to remove noise from EEG signals.

Jan 2021- Jun 2021
Gesture Controlled Robocar | *Chitkara University, Punjab, India*
- Designed a gesture-controlled robotic car using Arduino Uno, WEMOS D1 Mini, L298N motordriver, and PAJ7620 gesture sensor.
- Developed computer vision algorithms for gesture recognition and real-time motor control.

Extracurricular Activities

Aug 2021 - Aug 2022
Vice President IETE Student Chapter | *Chitkara University, India*
- Led the planning and execution of technical events, showcasing leadership and project management skills
- Delegated tasks and fostered team collaboration in a dynamic, multicultural environment
- Solved challenges proactively and adapted to changing circumstances with initiative and ownership

Jan 2020 - Aug 2022
Associate Editor for Departmental Newsletter, 'Electro-Bulletin' | *Chitkara University, Punjab, India*
- Managed content creation for the departmental newsletter.
- Oversaw assignments and ensured precise execution.
- Demonstrated strong logical thinking and attention to detail.
- Produced detailed documentation and reports.